

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Previously Presented) A liquid crystal display device, comprising:

- a first substrate;
- a main seal on the first substrate and defining a liquid crystal injection area;
- a first compensating layer under the main seal, said first compensating layer providing a step upon which the main seal is raised;
- a plurality of dummy seals on the first substrate and external to the liquid crystal injection area; and
- a second compensating layer under the plurality of dummy seals, the second compensating layer providing a step upon which the dummy seals are raised and having substantially a same structure as the first compensating layer.

Claim 2 (Original) The liquid crystal display device according to claim 1, wherein the main seal is provided with a liquid crystal injection hole through which a liquid crystal can be injected.

Claim 3 (Original) The liquid crystal display device according to claim 1, wherein the main seal and the dummy seals have a same thickness.

Claim 4 (Previously Presented) The liquid crystal display device according to claim 1, wherein the first compensating layer has a thickness of about 6500Å.

Claim 5 (Original) The liquid crystal display device according to claim 1, wherein a top of the main seal and tops of the dummy seals are a same distance from the first substrate.

Claim 6 (Original) The liquid crystal display device according to claim 1, further comprising:
a gate metal pattern on the substrate forming a gate line and a gate electrode; and
a gate-insulating layer covering the gate metal pattern.

Claim 7 (Previously Presented) The liquid crystal display device according to claim 6, wherein the first and second compensating layers include the gate metal pattern and the gate-insulating layer.

Claim 8 (Original) The liquid crystal display device according to claim 6, wherein the main seal and the dummy seals are formed on the gate-insulating layer.

Claims 9-20 (Canceled)

Claim 21 (Previously Presented) A liquid crystal display device, comprising:

a first substrate;
a main seal on the first substrate and defining a liquid crystal injection area;
a first compensating layer with a width substantially the same as a width of the main seal disposed between the first substrate and the main seal;
a plurality of dummy seals on the first substrate and external to the liquid crystal injection area; and

a second compensating layer with a width substantially the same as a width of the dummy seals disposed between the first substrate and the plurality of dummy seals, the second compensating layer having substantially a same structure as the first compensating layer.

Claim 22 (Previously Presented) The liquid crystal display device according to claim 21, wherein the main seal is provided with a liquid crystal injection hole through which a liquid crystal can be injected.

Claim 23 (Previously Presented) The liquid crystal display device according to claim 21, wherein the main seal and the dummy seals have a same thickness.

Claim 24 (Previously Presented) The liquid crystal display device according to claim 21, wherein the first compensating layer has a thickness of about 6500Å.

Claim 25 (Previously Presented) The liquid crystal display device according to claim 21, wherein a top of the main seal and tops of the dummy seals are a same distance from the first substrate.

Claim 26 (Previously Presented) The liquid crystal display device according to claim 21, further comprising:

a gate metal pattern on the substrate forming a gate line and an gate electrode; and
a gate-insulating layer covering the gate metal pattern.

Claim 27 (Previously Presented) The liquid crystal display device according to claim 26, wherein the first and second compensating layers include the gate metal pattern and the gate-insulating layer.

Claim 28 (Previously Presented) The liquid crystal display device according to claim 26, wherein the main seal and the dummy seals are formed on the gate-insulating layer.